

Accredited Standards Committee  
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Project:  
Ref Doc.:  
Reply to: Ernie Kohlwey

To: T11.3 Membership  
From: Ernie Kohlwey

Subject: Comments on state diagrams by Ernie Kohlwey

Bill,

Below are my comments on the diagrams in file T11/98-123v5.

Figure 1.2 Old Port Transition

Transition P1:OI0b should be labelled P1:I0b.

Paragraph 1.2.1

Transition F0:P1 should include the reception of NOS

Transition P1:OI0b should be labelled P1:I0b.

Figure 1.3 Loop Fail Initialization

Transition All:F0 condition of LP\_Bypass should be changed to Repeat.

Add Optional Transition F0:P1 To P1: OLD\_Port

Condition (optional): OLS | NOS recognized

Action Taken: -

Paragraph 1.3.1

Transition F0:OI0d should be labelled F0:OI0a.

Transition F0:OI0e should be labelled F0:OI0c.

Transition F0:OI0f should be labelled F0:OI0a.

Add description for transition F2:OI0a.

Figure 1.4 Normal Initialization

State IO: Normal Initialize entry action should include:

REPEAT = False.

Add Optional Transition I0:P1 To P1: OLD\_Port

Condition (optional): OLS | NOS recognized

Action Taken: -

State I1: Init\_Bypass. Is this a sub-state of Initializing state in FC-AL-2? Table 12 and 8.4.3 item 21 in FC-AL-2 would need to be changed so that LPB received or Req(bypass L\_port) don't go to Monitoring state but go there with LIP received when REPEAT is set or with ARB(f0) received when REPEAT is set. Is that what is being done?

Paragraph 1.4.1

Transition F0:OI0b should be labelled F0:OI0a.

Transition I1:M. The end of the first sentence should add "or ARB(F0)".

Add description for transition I0:P1.

Figure 1.5 Open\_Init State Machine

State OI2: OPEN\_INIT\_Bypass. Is this a sub-state of OPEN\_INIT state in FC-AL-2? Table 13 and 8.4.3 item 22 in FC-AL-2 would

need to be changed so that LPB received or Req(bypass L\_port) don't go to Monitoring state but go there with LIP received after AL\_Time when REPEAT is set or with ARB(f0) received when REPEAT is set. Is that what is being done?

Transition OI1:S0 from state OI0 should be labelled OI0:S0.

Transition OI1:F1 should be a dashed line arrow since the transition is optional.

Transition OI1:M0 Change the single "=" to "==" in two places.

Paragraph 1.5.1

Transition F0:OI0d description should be combined with the F0:OI0b description.

Transition F0:OI0e description should be combined with the F0:OI0c description.

Transition F0:OI0f description should be combined with the F0:OI0b description.

Add descriptions for transitions OI0:S0, OI1:M, and OI1:I0a

Figure 1.6 Slave Initialization

Add Transition OI0:S0 From OI0: OPEN\_INIT\_Start

Transition S0:S1 condition: Change

from: LPB recognized Y=AL\_PA or hex'EF

to: LPB recognized Y=AL\_PA or hex'FF | REQ(Bypass L\_Port)

State S1: Slave\_Bypass. Is this a sub-state of OPEN\_INIT

state in FC-AL-2? Table 13 and 8.4.3 item 22 in FC-AL-2 would need to be changed so that LPB received or Req(bypass L\_port)

don't go to Monitoring state but go there with LIP received

when REPEAT is set or with ARB(f0) received when REPEAT is set.

Is that what is being done?

Paragraph 1.6.1

Transition SL4:S11 should be labelled SL3:S11.

Add descriptions for transitions OI0:S0, S0:F1, S0:OI0c, and S1:M.

Figure 1.7 Slave Loop Map

Entry conditions for states SL1 and SL3, change

from: Start(LP\_TOV)

to: Start(LP\_TOV)

Figure 1.8 Master Initialization

Entry conditions for state M7, change

from: Set LIFL

to: Set LI\_FL

Paragraph 1.6.1

Transition M11:M should be labelled M10:M.

Annex ??, Example 2 description, fourth paragraph after Figure ??.2:

When Port Z enters the initializing loop fail state, it will transmit

LIP(F8) continuously until it receives the OLS. Switch Port Y will

be receiving the LIP(F8)s and will go to the OPEN\_INIT state.

How does Switch Port Y recognize LR from Port Z and get to the

Old-port state to transmit LRR? What state is the FL\_Port in when

it is in the "FL\_Port operating mode"? Is it the Monitoring state

and non-participating mode? Does Switch Port Y need to recognize

LR in the OPEN\_INIT state and transition to Old\_port state from it?

Annex ??, Example 3 description, first paragraph after Figure ??.3:

In the last sentence of the paragraph, change

from: "... recognize the OLD in Loop Initialization ..."

to: "... recognize the OLS in Loop Initialization ..."

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